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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/533,565	05/03/2005	Tohru Den	03500.017772	4469
5514 7590 06/28/2007 FITZPATRICK CELLA HARPER & SCINTO			EXAMINER	
30 ROCKEFEI	LER PLAZA		COLEMAN, WILLIAM D	
NEW YORK, NY 10112			ART UNIT	PAPER NUMBER
			2823	
			MAIL DATE	DELIVERY MODE
			06/28/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
Office A-4' Occurrence	10/533,565	DEN ET AL.			
Office Action Summary	Examiner	Art Unit			
	W. David Coleman	2823			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tin rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1)⊠ Responsive to communication(s) filed on 03 M	av 2005.				
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	<u>, </u>				
closed in accordance with the practice under E					
Disposition of Claims					
 4)⊠ Claim(s) 1-24 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 					
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1,2,5-8,10-13,15-19 and 22-24</u> is/are rejected.					
7) Claim(s) 3,4,9,14,20 and 21 is/are objected to.					
8) Claim(s) are subject to restriction and/or	election requirement.				
Application Papers					
9) The specification is objected to by the Examine	·				
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the					
Replacement drawing sheet(s) including the correcti					
11) The oath or declaration is objected to by the Ex					
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign	priority under 35 H.S.C. & 119/a)-(d) or (f)			
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:					
1. ☐ Certified copies of the priority documents have been received.					
2. Certified copies of the priority documents have been received in Application No					
3.⊠ Copies of the certified copies of the priority documents have been received in this National Stage					
application from the International Bureau (PCT Rule 17.2(a)).					
* See the attached detailed Office action for a list of the certified copies not received.					
Attachment(s)					
) X Notice of References Cited (PTO-892)	4) Interview Summary				
Notice of Draftsperson's Patent Drawing Review (PTO-948) Notice of Draftsperson's Patent Drawing Review (PTO-948) Notice of Informal Patent Application					
Paper No(s)/Mail Date <u>05/05</u> (2006)	6) Other:				
Patent and Trademark Office	tion Summany Pa	art of Paner No /Mail Date 20070619			

Application/Control Number: 10/533,565 Page 2

Art Unit: 2823

DETAILED ACTION

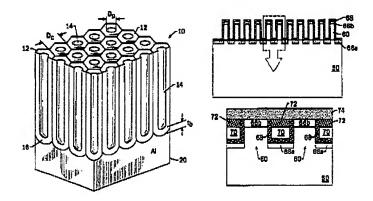
Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-2, 5-8, 10-13, 15-19 and 22-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Zhang et al., U.s. Patent 6,709,929 B2.

Zhang discloses a semiconductor nanostructure and a method of manufacturing a semiconductor nanostructure as claimed. See FIGS. 1-5F, where Zhang teaches the following limitations.



3. Pertaining to claim 1, <u>Zhang</u> teaches a nanostructure in the form of a mixture film which comprises a plurality of cylinders comprising Al as a major constituent, and a matrix region surrounding the plurality of cylinders and comprising Si and/or Ge,

wherein the total amount of Si and Ge is contained in a proportion in the range from 20 to 70 atomic % in the mixture film; the cylinders are orderly arrayed; the diameter of the cylinders in the range from 1 to 30 nm; and the interval between the cylinders is 30 nm and less (see Abstract).

- 4. Pertaining to claim 2, <u>Zhang</u> teaches the nanostructure according to claim 1, wherein the cylinders are orderly arrayed in a honeycomb array.
- 5. Pertaining to claim 5, <u>Zhang</u> teaches the nanostructure according to claim 1, wherein the average diameter of the cylinders is in the range from 2 to 8 nm.
- 6. Pertaining to claim 6, <u>Zhang</u> teaches the nanostructure according to claim 1, wherein the interval between the cylinders is 10 nm or smaller.
- 7. Pertaining to claim 7, Zhang teaches the nanostructure according to claim 1, wherein the matrix region is comprised of amorphous Si and/or amorphous Ge (see column 7, line 21).
- 8. Pertaining to claim 8, <u>Zhang</u> teaches the nanostructure according to claim 7, wherein the matrix region is amorphous Si.
- 9. Pertaining to claim 10, Zhang teaches the nanostructure according to claim 1, wherein the mixture film is formed on a substrate.

Art Unit: 2823

Pertaining to claim 11, <u>Zhang</u> teaches an electronic device according to claim 10, comprising wiring on part of the substrate.

10. Pertaining to claim 12, Zhang teaches a method of manufacturing a nanostructure in the form of a mixture film having a plurality of cylinders having a diameter in the range from 1 to 30 nm and an interval of 30 nm and less and comprising Al as a major constituent, and a matrix region surrounding the plurality of cylinders and comprising Si and/or Ge, the method comprising the steps of:

forming an ordered region for growing Al with priority on the substrate, and thereafter forming the mixture film which has Al and Si and/or Ge and in which the total amount of Si and Ge is contained in a proportion in the range from 20 to 70%, to fabricate the mixture film (please note that since the silicon can be deposited by a CVD process, it is well known that other materials are combined with the silicon such as chlorine to grow the silicon pillars).

- 11. Pertaining to claim 13, <u>Zhang</u> teaches the method of manufacturing a nanostructure according to claim 12, wherein the ordered region is a region having a honeycomb array or a pattern corresponding to part of the honeycomb array.
- 12. Pertaining to claim 15, <u>Zhang</u> teaches the method of manufacturing a nanostructure according to claim 12, wherein the ordered region for forming Al with priority includes a projection having Al as a major constituent.

Application/Control Number: 10/533,565 Page 5

Art Unit: 2823

13. Pertaining to claim 16, Zhang teaches the method of manufacturing a nanostructure according to claim 15, wherein the projection having Al as a major constituent is fabricated by anodization of a film having Al as a major constituent and etching of anodized Al film.

- 14. Pertaining to claim 17, Zhang teaches the method of manufacturing a nanostructure according to claim 12, wherein the method of forming the mixture film is a film forming method of forming a substance in a nonequilibrium state (please note that the gaseous state of the silicon is in a nonequilibrium state).
- 15. Pertaining to claim 18, <u>Zhang</u> teaches the method of manufacturing a nanostructure according to claim 17, wherein the film forming method of forming a substance in a nonequilibrium state is sputtering (see column 6, line 54).
- 16. Pertaining to claim 19, Zhang teaches the method of manufacturing a nanostructure according to claim 17, wherein the substrate temperature in film forming of forming a substance in a nonequilibrium state is 200°C or lower (please note that it is well known to form an amorphous silicon film by sputtering at room temperature which is well below 200°C).
- 17. Pertaining to claim 22, <u>Zhang</u> teaches the method of manufacturing a nanostructure according to claim 12, wherein the matrix region is Si.

Art Unit: 2823

- 18. Pertaining to claim 23, Zhang teaches a structure comprised by a first material and a second material, characterized in that a columnar in that a columnar member comprised by the first material is surrounded by a region comprised by the second material, that the second material in the structure is contained in a proportion in the range from 0 to 70 atomic % of the total amount of the first material and the second material, and that the columnar member is placed on a growth starting portion provided in advance.
- 19. Pertaining to claim 24, Zhang teaches a method of manufacturing a structure, characterized by having a step of preparing a substrate having a growth starting portion, and a step of forming a structure having on the substrate a columnar member comprised by a first material and a region comprised by a second material and surrounding the columnar member, the second material being contained in a proportion in the range from 20 to 70 atomic % of the total amount of the first material and the second material in the structure.

Objections

- 20. Claims 3, 4, 9, 14, 20 and 21 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 21. Normally it is customary to abide by the Trilateral Agreement in the prior art search in an international search report. Please note that the international search report suggested that claims 1-6 would be suggested by the prior art reference U.S. Patent 6,231,744 B1, however there is no

Application/Control Number: 10/533,565 Page 7

Art Unit: 2823

suggestion that explicitly teaches forming a SiGe nanostructure in a honeycomb. Therefore the Examiner believes that the additional prior art reference was necessary to address the present claims.

Specification

22. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Conclusion

- 23. Any inquiry concerning this communication or earlier communications from the examiner should be directed to W. David Coleman whose telephone number is 571-272-1856. The examiner can normally be reached on Monday-Friday 9:00 AM 5:30 PM.
- 24. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matt Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/533,565

Art Unit: 2823

25. Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Primary Examiner Art Unit 2823

Page 8

WDC